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ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.		PATENT DATE	
TR	SS ²	Q.A.	OUT

APPLICATION NO. 09/724196	CONT/PRIOR	CLASS 712	SUBCLASS 234	ART UNIT 2183	EXAMINER <i>Chan, Eddie</i>
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APPLICANTS

37111

John Redford

Branch handling for single instruction multiple datapath processor architectures

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED 	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ _____ _____	_____ _____ (Primary Examiner) (Date)			ISSUE FEE Amount Due Date Paid	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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